

438	14	Class	Subclass
		ISSUE C	

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

AK SCANNED S.O. Q.A. [Signature]

APPLICATION NO. 09/869274	CONT/PRIOR D	CLASS 438	SUBCLASS 108	ART UNIT <sup>2012</sup> 3823	EXAMINER Coleman W. R. Patterson
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## APPLICANTS

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Semiconductor integrated circuit and manufacture thereof

**FILE**

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>				
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>				
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		<b>ISSUE FEE</b>  <table border="1"> <tr> <td>Amount Due</td> <td>Date Paid</td> </tr> <tr> <td>_____</td> <td>_____</td> </tr> </table>		Amount Due	Date Paid	_____
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